



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#24
3/21/03
Moorish

Application Serial No. 09/603,147
Filing Date June 23, 2000
Inventor John T. Moore et al.
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Kielin, E.
Attorney's Docket No. MI22-1443
Title: Capacitor Constructions, DRAM Constructions and Semiconductive Material
Assemblies

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

To: Assistant Commissioner for Patents
Washington, D.C. 20231
From: Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Dear Sir:

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 and copies of which are attached.
Citation of these references is respectfully requested.

RECEIVED
MAR 20 2003
TECHNOLOGY CENTER 2800

Respectfully submitted,

Dated:

March 19, 2003

By:

Jennifer J. Taylor, Ph.D.
Reg. No. 48,711